

L Number	Hits	Search Text	DB	Time stamp
5	316	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (current with source) and (transistor with voltage with source) and ((current adj source) with (voltage adj source)) and (transistor with ground with (current adj source))	USPAT; US-PGPUB	2003/07/08 10:29
6	16	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (current with source) and (transistor with voltage with source) and ((current adj source) with (voltage adj source)) and (transistor with ground with (current adj source))) and (test\$3 with power)	USPAT; US-PGPUB	2003/07/08 11:28
7	316	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage adj2 source) and (current adj2 source) and (transistor with voltage with source) and ((current adj source) with (voltage adj source)) and (transistor with ground with (current adj source))	USPAT; US-PGPUB	2003/07/08 10:32
8	291	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage adj2 source) and (current adj2 source) and (transistor with (voltage adj2 source)) and ((current adj source) with (voltage adj source)) and (transistor with ground with (current adj source))	USPAT; US-PGPUB	2003/07/08 10:33
9	14	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage adj2 source) and (current adj2 source) and (transistor with (voltage adj2 source)) and ((current adj source) with (voltage adj source)) and (transistor with ground with (current adj source))) and (test\$3 with power)	USPAT; US-PGPUB	2003/07/08 11:22
10	1291	((current adj2 source) with (couple\$2 or connect\$2) with (voltage adj2 source)) and (transistor with (couple\$2 or connect\$2) with (current adj2 source)) and (transistor with ground)	USPAT; US-PGPUB	2003/07/08 11:27
11	56	((current adj2 source) with (couple\$2 or connect\$2) with (voltage adj2 source)) and (transistor with (couple\$2 or connect\$2) with (current adj2 source)) and (transistor with ground)) and (test\$3 with power)	USPAT; US-PGPUB	2003/07/08 11:28
14	8	702/\$.ccls. and (testing with (microprocessor or CPU or computer or microcomputer) with power) and (voltage with source) and (control with circuit)	USPAT; US-PGPUB	2003/07/08 13:05
15	3	702/\$.ccls. and (testing near5 (microprocessor or CPU or computer or microcomputer or microelectronic) near5 power) and (voltage with source) and (control with circuit)	USPAT; US-PGPUB	2003/07/08 13:10
16	17	702/\$.ccls. and (testing near3 power) and (voltage with source) and (control with circuit)	USPAT; US-PGPUB	2003/07/08 13:11
-	1	10/053973	USPAT; US-PGPUB	2003/07/06 16:15
-	1	assembly same validat\$3 same (microelectronic with power with regulation with system)	USPAT; US-PGPUB	2003/06/09 11:27

-	1	assembly same validat\$3 same (power with regulation with system)	USPAT; US-PGPUB	2003/06/09 11:27
-	2	assembly same validat\$3 same (power with regulator)	USPAT; US-PGPUB	2003/06/09 11:32
-	1	(assembly same validat\$3 same (power with regulator)) and capacitor and substrate	USPAT; US-PGPUB	2003/06/09 11:29
-	1	assembly and (microelectronic with power with regulat\$4) and substrate and transient and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:35
-	1	assembly and (microelectronic with power with regulat\$4) and substrate and transient and capacitor	USPAT; US-PGPUB	2003/06/09 11:35
-	136	assembly and (power with regulat\$4) and substrate and transient and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:38
-	4	(assembly and (power with regulat\$4) and substrate and transient and amplitude and capacitor) and (spatial with control with power)	USPAT; US-PGPUB	2003/06/09 11:36
-	1	assembly and (power adj regulat\$4) and substrate and (transient adj generator) and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:39
-	1	assembly and (power with regulat\$4) and substrate and (transient adj generator) and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:41
-	14	assembly and (power with regulat\$4) and substrate and (transient adj event) and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:44
-	1	assembly and (power adj regulator) and substrate and (transient adj generator) and capacitor	USPAT; US-PGPUB	2003/06/09 11:57
-	1	assembly and (power adj regulator) and substrate and (transient adj generator)	USPAT; US-PGPUB	2003/06/09 11:43
-	1	(power adj regulator) and substrate and (transient adj generator)	USPAT; US-PGPUB	2003/06/09 11:43
-	4	assembly and (power with regulator) and substrate and (transient adj event) and amplitude and capacitor	USPAT; US-PGPUB	2003/06/09 11:50
-	1	(assembly and (microelectronic with power with regulat\$4) and substrate) and transient	USPAT; US-PGPUB	2003/06/09 11:47
-	7	assembly and (microelectronic with power with regulat\$4) and substrate	USPAT; US-PGPUB	2003/06/09 12:06
-	4	(assembly and (power with regulator) and substrate and (transient adj event) and amplitude and capacitor) and (microprocessor or computer or CPU or microcomputer)	USPAT; US-PGPUB	2003/06/09 11:51
-	1	assembly and (power adj regulator) and substrate and (transient adj generator)	USPAT; US-PGPUB	2003/06/09 11:58
-	1	substrate same (transient adj generator)	USPAT; US-PGPUB	2003/06/09 11:58
-	31	substrate same (transient with generator)	USPAT; US-PGPUB	2003/06/09 12:00
-	1	(substrate same (transient with generator)) and (power with regulation with system)	USPAT; US-PGPUB	2003/06/09 12:00
-	33	(power with regulation with system) and (transient with generator)	USPAT; US-PGPUB	2003/06/09 12:00
-	1	((power with regulation with system) and (transient with generator)) and substrate	USPAT; US-PGPUB	2003/06/09 12:02
-	21	((power with regulation with system) and (transient with generator)) and capacitor	USPAT; US-PGPUB	2003/06/09 12:03
-	5	((power with regulation with system) and (transient with generator)) and capacitor) and assembly	USPAT; US-PGPUB	2003/06/09 12:07
-	1	((power with regulation with system) and (transient with generator)) and capacitor) and assembly) and surface	USPAT; US-PGPUB	2003/06/09 12:07

-	4	("5394025" "5886515" "5942886" "4042830").pn.	USPAT; US-PGPUB	2003/07/06 16:17
-	0	((("5394025" "5886515" "5942886" "4042830").pn.) and (testing with power with regulator)	USPAT; US-PGPUB	2003/07/06 16:32
-	53	(testing with power with regulator)	USPAT; US-PGPUB	2003/07/06 16:18
-	1	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 16:35
-	15	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor	USPAT; US-PGPUB	2003/07/06 16:22
-	1	((testing with power with regulator)) and (voltage with source) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 16:37
-	1	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor) and microelectronic	USPAT; US-PGPUB	2003/07/06 16:24
-	2	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor) and (gate with region)	USPAT; US-PGPUB	2003/07/06 16:25
-	2	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor) and (gate with region)) and (current with source)	USPAT; US-PGPUB	2003/07/06 16:24
-	3	((testing with power with regulator)) and (voltage with source) and (control with circuit) and transistor) and (trigger with signal with input)	USPAT; US-PGPUB	2003/07/06 16:26
-	2	((("5394025" "5886515" "5942886" "4042830").pn.) and (testing with power)	USPAT; US-PGPUB	2003/07/06 16:30
-	0	((("5394025" "5886515" "5942886" "4042830").pn.) and (testing with microelectronic with power)	USPAT; US-PGPUB	2003/07/06 17:30
-	8	(testing with microelectronic with power)	USPAT; US-PGPUB	2003/07/06 16:33
-	1	((testing with microelectronic with power)) and (voltage with source) and (control with circuit) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 17:17
-	1	((testing with microelectronic with power)) and (voltage with source) and (control with circuit) and transistor and (current with source)	USPAT; US-PGPUB	2003/07/06 16:36
-	1	((testing with microelectronic with power)) and (voltage with source) and (control with circuit) and (current with source)	USPAT; US-PGPUB	2003/07/06 17:32
-	5	((testing with microelectronic with power)) and (voltage with source) and (control with circuit)	USPAT; US-PGPUB	2003/07/06 17:06
-	1	((testing with microelectronic with power)) and (voltage with source) and (control with circuit)) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 16:42
-	5	((testing with microelectronic with power)) and (voltage with source) and (control with circuit)) and transistor	USPAT; US-PGPUB	2003/07/06 16:40
-	1	((testing with microelectronic with power)) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 17:17
-	1	((testing with power with regulator)) and transistor and (drain with region)	USPAT; US-PGPUB	2003/07/06 16:42
-	26821	transistor with (drain with region)	USPAT; US-PGPUB	2003/07/06 16:45
-	1	(transistor with (drain with region)) and ((testing with power with regulator))	USPAT; US-PGPUB	2003/07/06 16:44

-	1	(transistor with (drain with region)) and	USPAT;	2003/07/06
-	4400	(transient with load with generator)	US-PGPUB	16:45
-		(voltage with source) with transistor	USPAT;	2003/07/06
-	1579	with (drain with region)	US-PGPUB	16:47
-		((voltage with source) with transistor	USPAT;	2003/07/06
-	2	with (drain with region)) and (voltage	US-PGPUB	16:49
-		with source) and (control with circuit)		
-	2	((voltage with source) with transistor	USPAT;	2003/07/06
-		with (drain with region)) and (voltage	US-PGPUB	16:49
-	0	with source) and (control with circuit)		
-		and transistor and (gate with region)		
-		((("5394025" "5886515" "5942886"	USPAT;	2003/07/06
-		"4042830").pn.) and (testing with power)	US-PGPUB	17:17
-) and (voltage with source) and (control		
-	2	with circuit) and transistor) and (drain		
-		with region)		
-	2	((("5394025" "5886515" "5942886"	USPAT;	2003/07/06
-		"4042830").pn.) and (testing with power)	US-PGPUB	17:18
-) and (voltage with source) and (control		
-	512	with circuit) and transistor		
-		(testing with (microprocessor or CPU or	USPAT;	2003/07/06
-	68	computer or microcomputer) with power)	US-PGPUB	17:52
-		((testing with (microprocessor or CPU or	USPAT;	2003/07/06
-		computer or microcomputer) with power))	US-PGPUB	17:37
-		and (voltage with source) and (control		
-	2	with circuit) and (current with source)	USPAT;	2003/07/06
-		((testing with (microprocessor or CPU or	US-PGPUB	17:34
-		computer or microcomputer) with power))		
-		and (voltage with source) and (control		
-		with circuit) and (current with source))		
-		and transistor and (gate with region) and		
-	2	(drain with region)		
-		((testing adj5 (microprocessor or CPU or	USPAT;	2003/07/06
-		computer or microcomputer) adj2 power))	US-PGPUB	17:37
-		and (voltage with source) and (control		
-	16	with circuit) and (current with source)		
-		(testing adj5 (microprocessor or CPU or	USPAT;	2003/07/06
-		computer or microcomputer) adj2 power)	US-PGPUB	17:45
-	2	(testing with (microprocessor or CPU or	USPAT;	2003/07/07
-		computer or microcomputer) with power)	US-PGPUB	16:24
-		and (transient with load with generator)		
-	1	(testing with (microprocessor or CPU or	USPAT;	2003/07/08
-		computer or microcomputer) with power)	US-PGPUB	13:02
-		and (transient with load with generator)		
-		and (voltage with source) and (control		
-		with circuit)		
-	27	(transient with load with generator) and	USPAT;	2003/07/07
-		(first with voltage with source) and	US-PGPUB	16:52
-		(second with voltage with source)and		
-		(control with circuit)		
-	3	((transient with load with generator) and	USPAT;	2003/07/07
-		(first with voltage with source) and	US-PGPUB	16:37
-		(second with voltage with source)and		
-		(control with circuit)) and drain and		
-		gate and transistor		
-	1	(transient with load with generator) and	USPAT;	2003/07/07
-		(voltage with source) and (control with	US-PGPUB	17:17
-		circuit) and (drain with transistor with		
-		voltage with source) and (gate with		
-		region with transistor with control with		
-		circuit) and transistor		
-	1	(transient with load with generator) and	USPAT;	2003/07/07
-		(voltage with source) and (control with	US-PGPUB	17:02
-		circuit) and (drain with transistor) and		
-		(gate with region with transistor) and		
-		transistor		

-	1	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (transient with load with generator) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor	USPAT; US-PGPUB	2003/07/07 17:13
-	929	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor	USPAT; US-PGPUB	2003/07/07 17:34
-	334	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (voltage with drain with region with transistor) and (voltage with gate with region with transistor) and transistor	USPAT; US-PGPUB	2003/07/07 17:19
-	8	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (voltage with drain with region with transistor) and (voltage with gate with region with transistor) and transistor) and (transient with load)	USPAT; US-PGPUB	2003/07/07 17:20
-	1	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (control with circuit with drain with region with transistor) and (voltage with gate with region with transistor) and transistor) and (transient with load)	USPAT; US-PGPUB	2003/07/07 17:49
-	1	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (control with circuit with drain with region with transistor) and (voltage with gate with region with transistor) and transistor) and (testing with (microprocessor or CPU or computer or microcomputer) with power)	USPAT; US-PGPUB	2003/07/07 17:22
-	38	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (control with circuit with drain with region with transistor) and (voltage with gate with region with transistor) and transistor	USPAT; US-PGPUB	2003/07/07 17:21

-	2	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (drain with transistor) and (gate with region with transistor) and transistor) and (control with circuit with drain with region with transistor) and (voltage with gate with region with transistor) and transistor) and ((microprocessor or CPU or computer or microcomputer) with power)	USPAT; US-PGPUB	2003/07/07 17:22
-	303	(702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (current with source) and (control with circuit with trigger with signal) and switch and transistor	USPAT; US-PGPUB	2003/07/07 17:38
-	12	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (current with source) and (control with circuit with trigger with signal) and switch and transistor) and (transistor with voltage with source) and ((current adj source) with (voltage adj source))	USPAT; US-PGPUB	2003/07/08 10:02
-	1	((702/\$.ccls. or 324/\$.ccls. or 323/\$.ccls. or 327/\$.ccls. or 307/\$.ccls.) and (voltage with source) and (control with circuit) and (current with source) and (control with circuit with trigger with signal) and switch and transistor) and (transistor with voltage with source) and ((current adj source) with (voltage adj source))) and (transient with load)	USPAT; US-PGPUB	2003/07/07 17:49